

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1-2. (cancelled)

3. (currently amended) [[The]] A functional block according to claim 2 comprising:  
a system bus synchronizing with a system clock;  
a functional circuit synchronizing with a local clock; and  
a synchronizing circuit for determining the operation timing for signal exchange between  
said system bus and said functional circuit,

wherein said synchronizing circuit takes in said system clock and said local clock and  
determines said operation timing by determining the access time of said functional circuit from  
said system clock,

wherein said synchronizing circuit takes in said system clock and said local clock and  
determines said operation timing by creating an access end signal for said functional circuit from  
said system clock, and

wherein said access end signal is created by using a counter actuated in synchronization  
with said system clock and a comparing circuit to compare the output of said counter with a  
frequency specifying signal.

4-12. (cancelled)

13. (currently amended) [[The]] A semiconductor integrated circuit according to  
claim 12 comprising:

a system bus synchronizing with a system clock;  
a functional circuit synchronizing with a local clock; and  
a synchronizing circuit for determining the operation timing for signal exchange between  
said system bus and said functional circuit,  
wherein said synchronizing circuit takes in said system clock and said local clock and  
determines said operation timing by determining the access time for said functional circuit from  
said system clock,  
wherein said synchronizing circuit takes in said system clock and said local clock and  
determines said operation timing by creating an access end signal for said functional circuit from  
said system clock,  
wherein said access end signal is created by using a counter actuated in synchronization  
with said system clock and a comparing circuit to compare the output of said counter with a  
frequency specifying signal.

14 – 20 (cancelled)

21. (new) The functional block according to claim 3, wherein the frequency of said  
system clock is not fixed to a specific frequency.

22. (new) The functional block according to claim 3, wherein the frequency of said  
system clock is given to create design data at a register transfer level automatically.

23. (new) The semiconductor integrated circuit according to claim 13, wherein the  
frequency of said system clock is not fixed to a specific frequency.

24. (new) The semiconductor integrated circuit according to claim 13, wherein the frequency of said system clock is given to create design data at a register transfer level automatically.